

IN THE CLAIMS:

1. (Previously Presented) A method of processing errors in a computer system, having at least one processor, memory, and a bus coupled between the memory and the processor, comprising:
 - identifying, by a service processor, failed hardware of the computer system;
 - identifying, by the service processor, other hardware affected by the failed hardware within the computer system, wherein the other hardware comprises all functional units associated with a common bus interface error;
 - deconfiguring the failed hardware and the other hardware affected by the failed hardware;
 - and
 - rebooting the computer system without running a diagnostic on the failed hardware.
2. (Previously Presented) The method of claim 1, wherein the deconfiguring and rebooting steps are performed by the service processor.
3. (Canceled)
4. (Original) The method of claim 1, wherein the step of deconfiguring includes activating at least one switch of circuitry of the computer system such that the failed hardware is excluded from the computer system.
5. (Previously Presented) The method of claim 1, wherein the service processor identifies the failed hardware in a table entry in a second memory indicating that the failed hardware has an error, and wherein the other hardware affected by the failed hardware is further identified in the table indicating the other hardware is associated with the failed hardware.
- 6-23. (Canceled)